

EAST - [067820c:1]

FileViewEditToolsWindowHelp

Active

L1: (315) (hierarch\$4 near20(connect\$4 or interconnect\$

L2: (8025) block near20 priority

L3: (8627) block near20 hierarch\$4

L4: (119945) power near9 (select\$4 or allowable)

L5: (26) 11 and 12 and 13 and 14

L6: (63460) block near9 cell

L7: (3) 11 and 12 and 13 and 14 and 16

L8: (114185) block same cell

L10: (377215) block near9 (device or component)

L11: (25) 15 and 110

L9: (3) 15 and 18

Failed

Search

Find

Browse

Clear

DO: US PGF08 USPAT EPO.1

☒ Paraph

Default operator: OR

☒ Highlight all hit terms initially

15 and 18

BRS form

ISIR form

Image

Text

HTML

	U	I	Document ID	Iss	Title	Current	Ci
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020073381	20	Method for designing integrated circuit device	716/1	71
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6415416 B1	20	Method for improving the efficiency of designing a system-on	716/2	
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 2000123059	20	METHOD FOR DESIGNING INTEGRATED CIRCUIT DEVICE		

Hits

Details

HTML

Ready

NUM